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Information Disclosure Statement by Applicant						Applicant: Teener et al.			
(Use several sheets if necessary)						Filed: December 31, 2003 Group: <del>2835</del> <b>2836</b>			
U.S. Patent Documents									
Init.		Document No.	Date	Name	Class	Subclass	Filing Date		
Foreign Documents									
								Translation	
Init.		Document No.	Date	Country	Class	Subclass	Yes	No	
<i>RW</i>	1	EP 1 085 706 A2	03/21/2001	Europe	H04L	12/46		X	
<i>RW</i>	2	EP 1 085 706 A3	10/30/2002	Europe	H04L	12/46		X	
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)									
<i>RW</i>	3	Bregni et al., "Jitter Testing Technique and Results at VC-4 Desynchronizer Output of SDH Equipment, <u>IEEE International Conference on Communications</u> , Vol. 3, pp. 1407-1410, May 12, 1994.							
<i>RW</i>	4	"Information technology-Microprocessor systems--Control and Status Registers (CSR) Architecture for microcomputer buses", ANSI/IEEE Standard 1212, The Institute of Electrical and Electronics Engineers, Inc. pp. I-122, 1994 Edition. <i>No Month</i>							
<i>RW</i>	5	Bregni et al., "Jitter Testing Technique and Results at VC-4 Desynchronizer Output of SDH Equipment, <u>IEEE Transactions on Instrumentation and Measurement</u> , Vol. 44, Issue 3, pp. 675-678, June 1995.							
<i>RW</i>	6	"IEEE Standard for a High Performance Serial Bus", IEEE Standard 1394-1995, Institute of Electrical and Electronics Engineers, Inc., pp. I-384, approved July 22, 1996.							
<i>RW</i>	7	Shiwen et al., "Parallel Positive Justification in SDH C <sub>4</sub> Mapping, <u>IEEE International Conference on Communications</u> , Vol. 3, pp. 1577-1581, June 12, 1997							
<i>RW</i>	8	"AV/C Digital Interface Command Set General Specification, Rev. 3.0", 1394 Trade Association, pp. 4-5, 20-34, April 15, 1998.							
<i>RW</i>	9	"Enhancements to the AV/C General Specification 3.0 Version 1.0FC1", 1394 Trade Association, pp. 4, 6-17, November 5, 1998.							
<i>RW</i>	10	"Information Technology-Fibre Channel-Methodologies for Jitter Specification", NCITS TR-25-1999, Jitter Working Group Technical Report, Rev. 10, pp. 1-96, June 9, 1999.							
<i>RW</i>	11	"P1394a Draft Standard for a High Performance Serial Bus (Supplement)", Draft 3.0, Institute of Electrical and Electronics Engineers, Inc., pp. 1-187, June 30, 1999.							
<i>RW</i>	12	"IEEE Standard for a High Performance Serial Bus-Amendment 1", Institute of Electrical and Electronics Engineers, Inc., pp. 1-196, approved March 30, 2000.							
<i>RW</i>	13	"P1394b IEEE Draft Standard for a High Performance Serial Bus (High Speed Supplement) P1394b Draft 1.3.3, Institute of Electrical and Electronics Engineers, Inc., pp. 1-408, November 16, 2001.							
<i>RW</i>	14	"IEEE Standard for a High Performance Serial Bus-Amendment 2", Institute of Electrical and Electronics Engineers, Inc., pp. 1-369, 2002 (no month).							
Examiner					Date Considered				
<i>Donald W. Liza</i>					<i>1/15/05</i>				
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.									